

FIG. 1

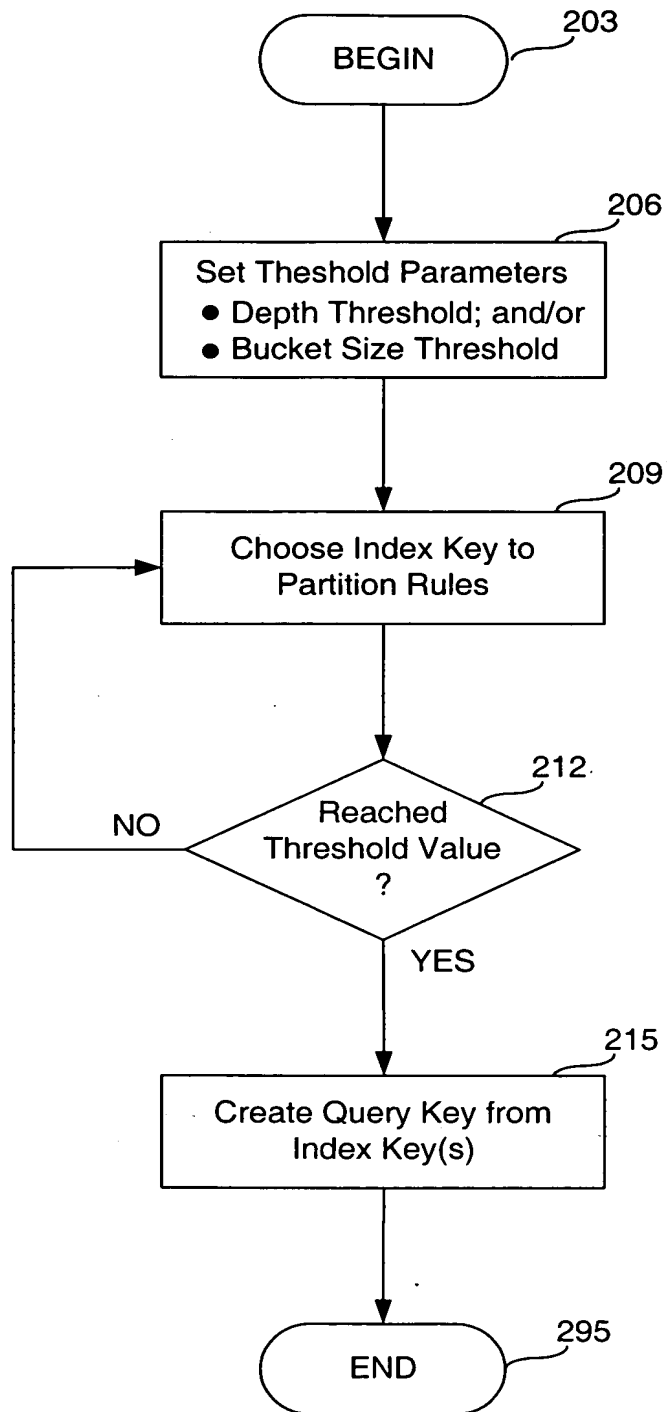


FIG. 2

input bits

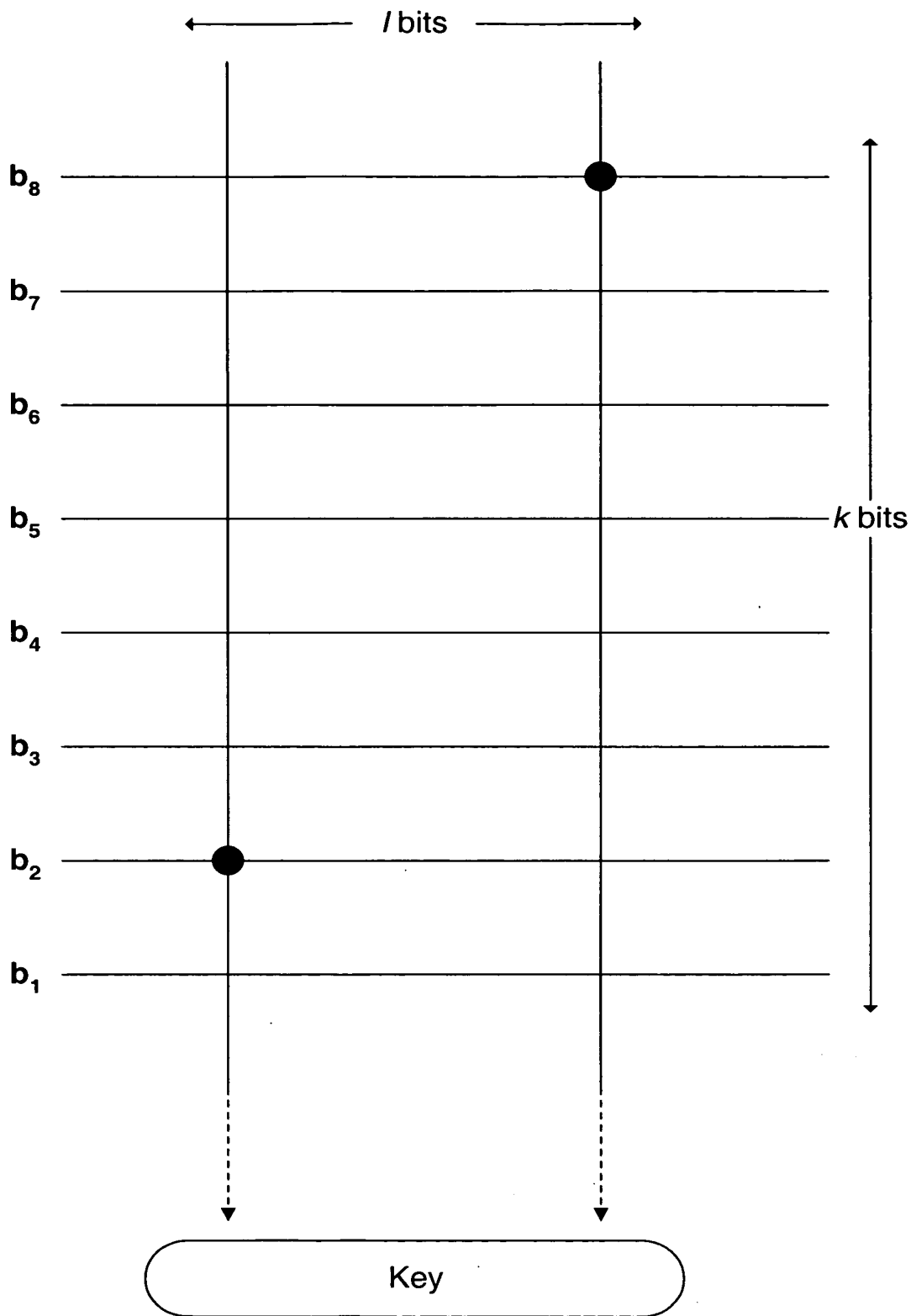


FIG. 3

FIG. 4 is a block diagram of a system 100.

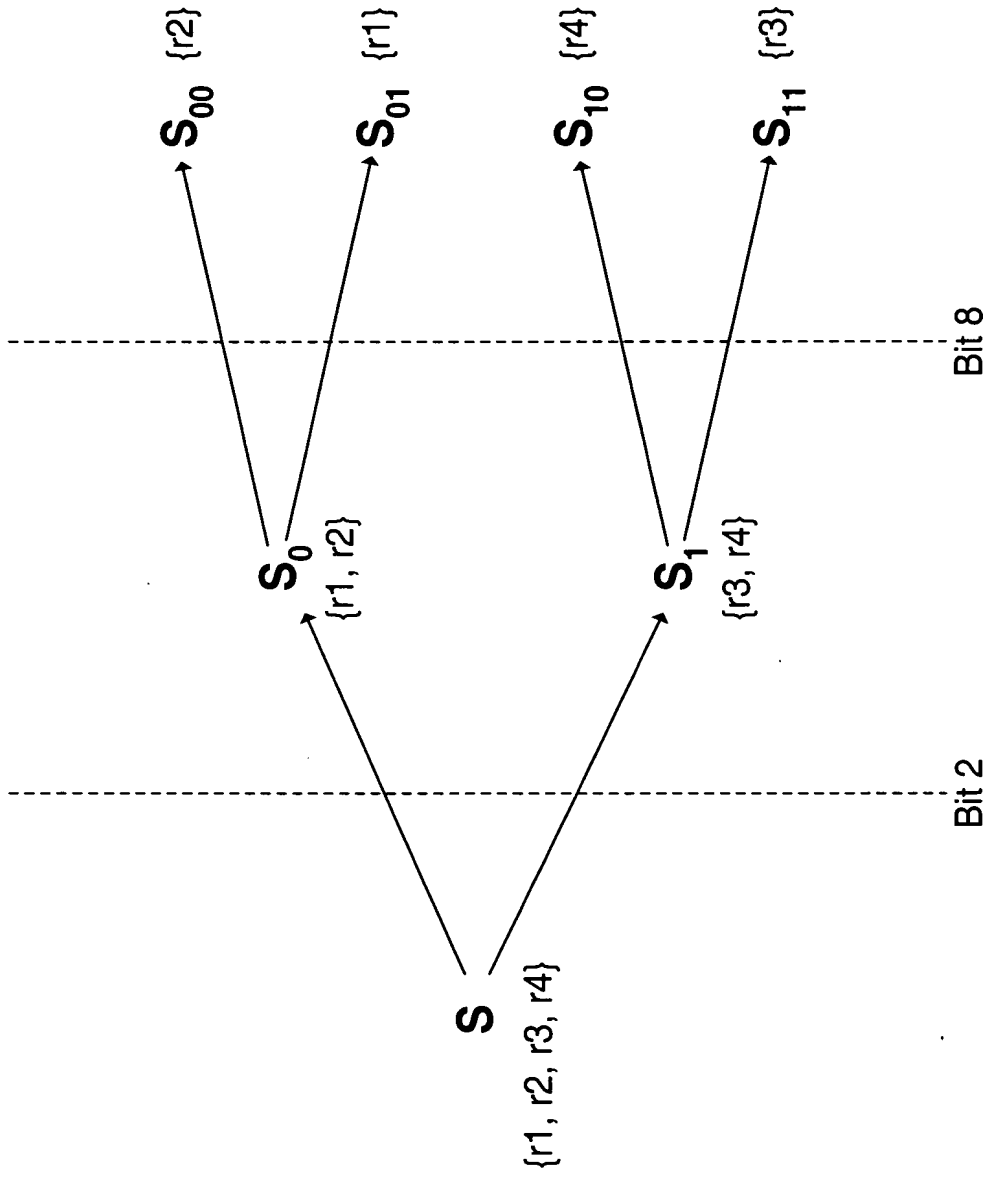


FIG. 4

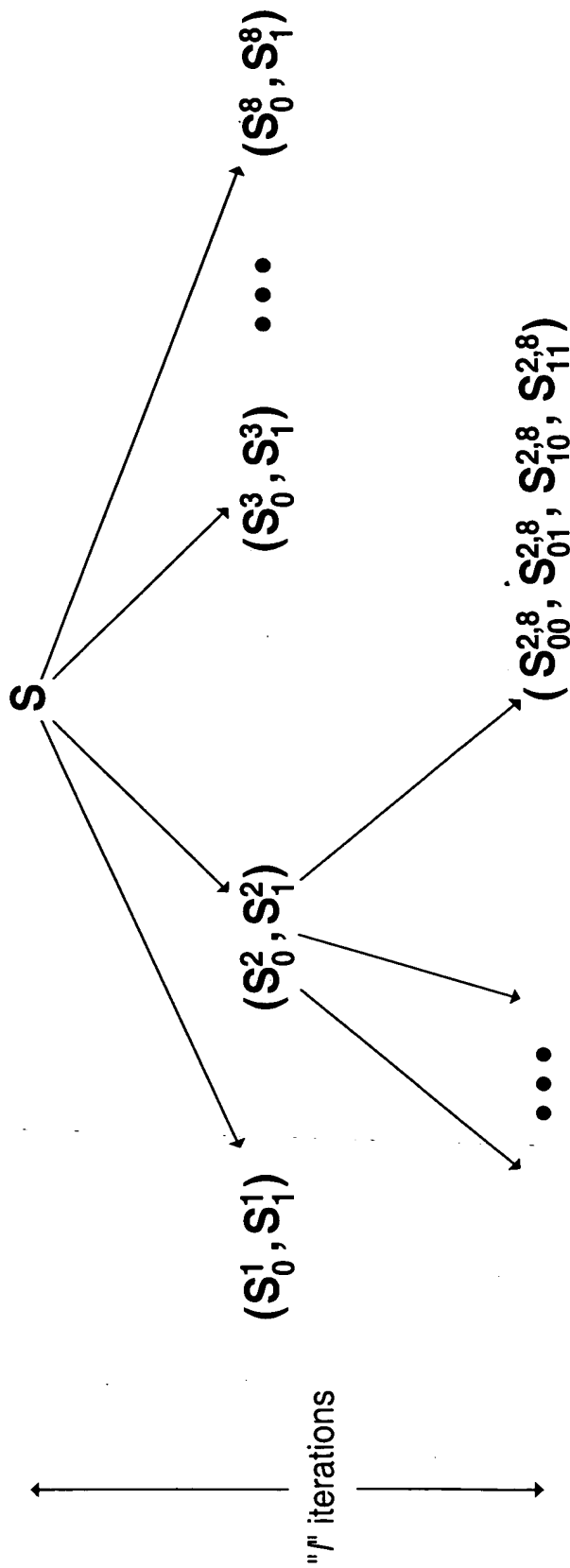


FIG. 5

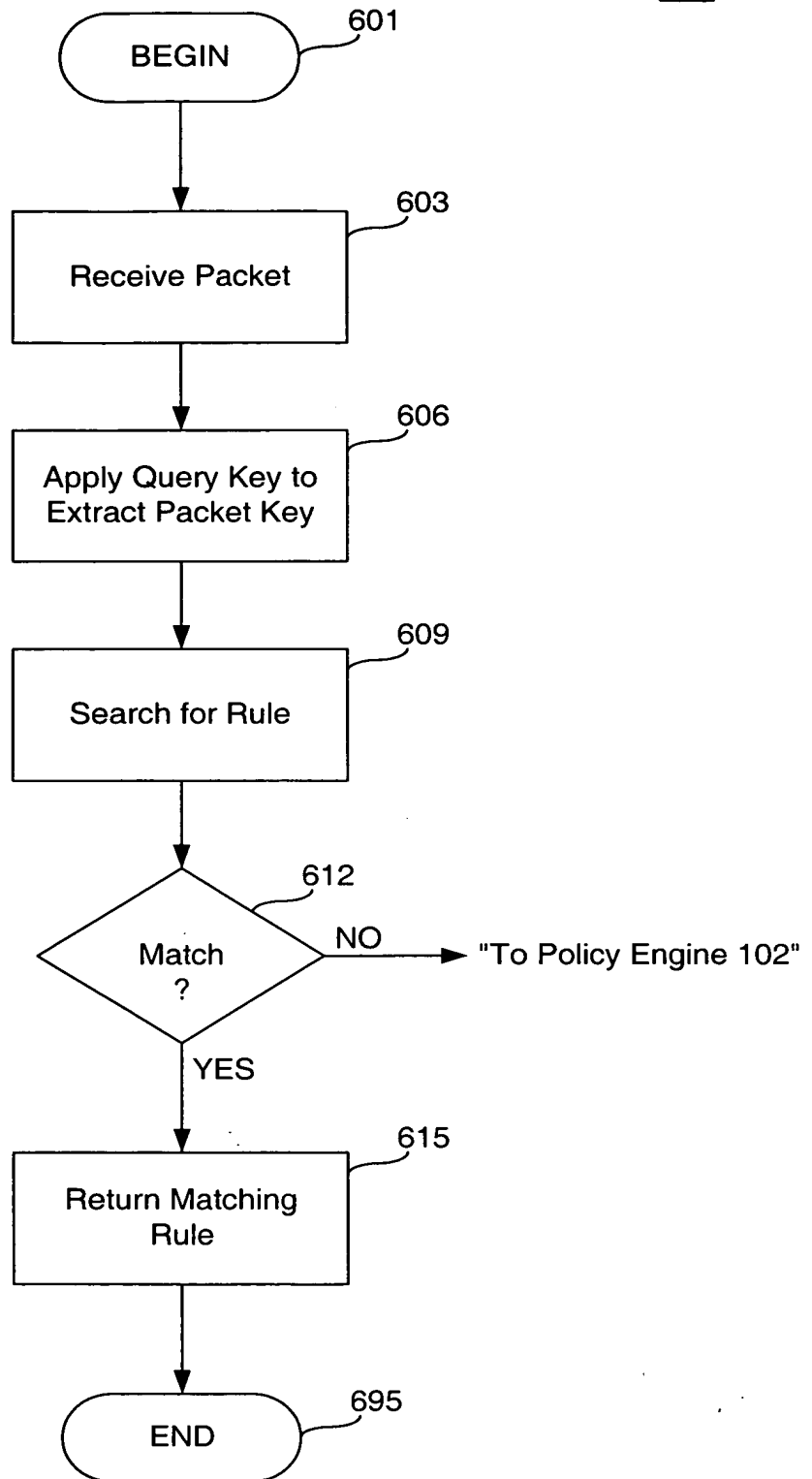


FIG. 6



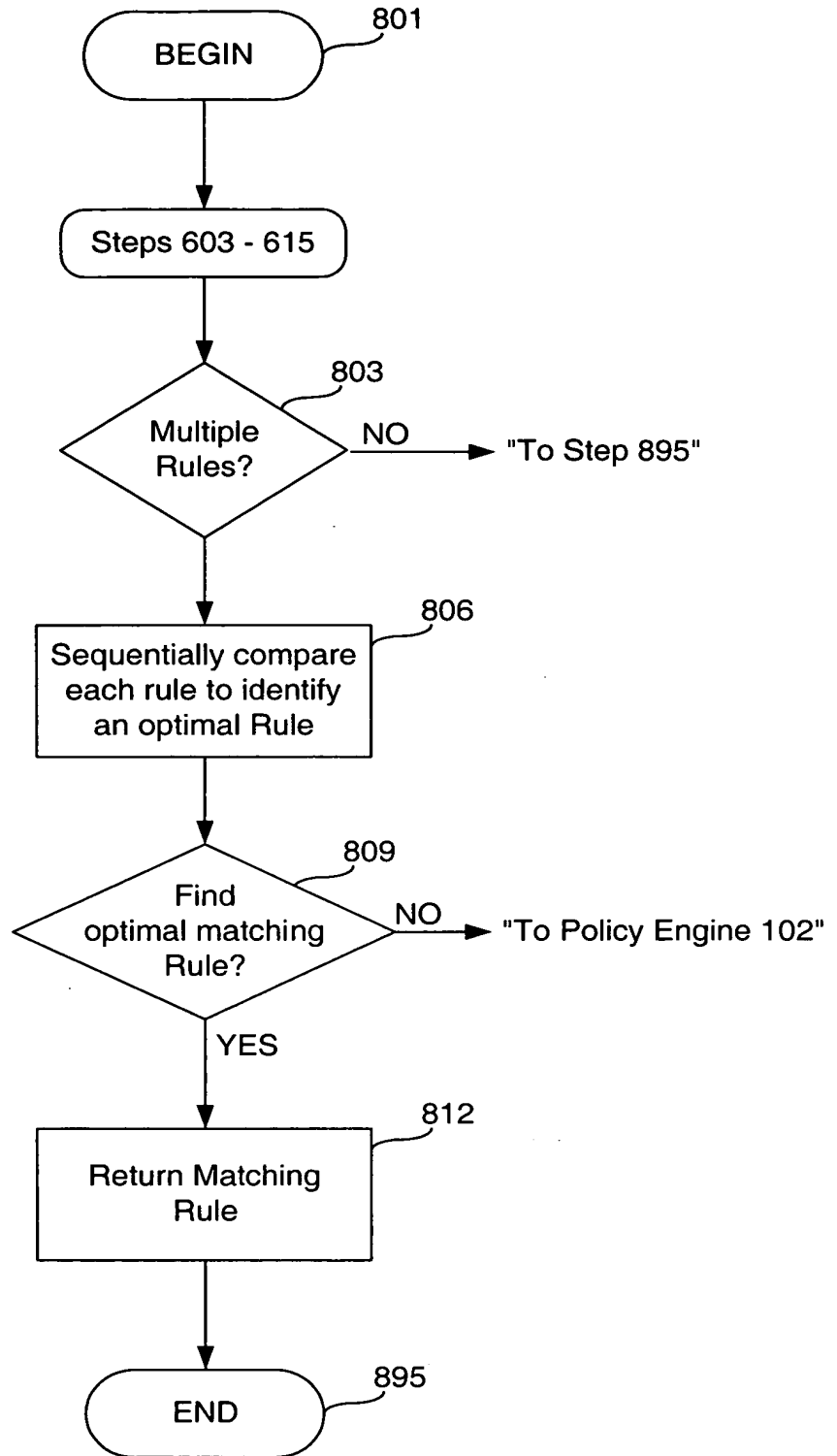


FIG. 8